

II. SPECIFICATION AMENDMENTS

Please amend the paragraph on page 3, lines 11-15 as follows:

(8) antenna Tx1h for broadcasting the message at a frequency detectable by antenna Rx2a of transceiver 2 and recoverable by transceiver 2 wherein, the two antenna ~~Rx1h~~Tx1h and Rx2a define a transmission or communication channel between transceivers 1 and 2.

Please amend the paragraph on page 11, line 18 through page 12, line 5 as follows:

In addition, the prior art receiver Rx2b of transceiver 2 may initially sync with incoming remnants of a message hit by a short J pulse. However, the downstream elements of the receiver are soon likely to lose sync upon receipt of inverted data bit pairs that are by-products of a J pulse hitting a message within the transmission channel. The inverted bit pairs are forwarded to the de-interleaver Rx2d. The de-interleaver is not able to sync with inverted bit pairs. Therefore, the incoming data remnants no longer flow to the downstream elements including the FEC decoder Rx2e, the DDE decoder Rx2f, the de-encrypter Rx2g and the DMUX Rx2h. The transceiver 2 loses synchronization with the message transmitted from transceiver 1 until a sufficient number of bits have been received to allow transceiver 2 to resynchronize. Independent resynchronization of the ~~DE-IDI~~ Rx2d, FEC Decode Rx2e, DDE Rx2f, De-crypt Rx2g and DMUX Rx2h elements is required.

Please amend the paragraph on page 12, line 23 through page 13, line 2 as follows:

A second experiment involved relocating the differential encoder ("DE") Tx1c of transceiver 1 between the FEC encoder Tx1d and the interleaver ~~Tx1f~~Tx1e. In addition, the differential decoder ("DDE") ~~Rx2d~~Rx2f of transceiver 2 was relocated between FEC decoder Rx2e and the de-interleaver (~~"D-IDI"~~) Rx2d. This embodiment performed poorly against long J pulses.

Please amend the paragraph on page 13, line 26 through page 14, line 14 as follows:

With reference to transceivers 3 and 4 of fig. 2, DDE Rx4d is located close to the output of receiver Rx4b to synchronize immediately with the first data bit pair or inverted bit pair passed to the DDE from receiver Rx4b, whether the bit pair is inverted or not. The DDE forwards both natural bit pairs and righted bit pairs to the downstream elements including the (~~"De-IDI"~~) Rx4e and FEC decoder Rx4f. Therefore, once a J pulse expires, receiver Rx4 of transceiver 4 is able to receive, process and pass the balance of incoming remnant data segments of a damaged message to the downstream elements from DDE Rx4d to DMUX Rx4h. Each of downstream elements from DDE Rx4d to DMUX Rx4h synchronizes on received data bit pairs, or inverted bit pairs that are righted by DDE Rx5dh. Therefore, the new and improved systems of fig 2 are able to recover substantial remnant data produced by a J pulse hit on a transmitted message.